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In one embodiment, the system 100 is configured to support microcontroller design. In another embodiment, the system 100 is configured to support programmable microcontroller design. In yet another embodiment, the system 100 supports general chip design.

VERSION OF AMENDMENTS WITH CHANGES SHOWN:

IN THE SPECIFICATION

Please replace the paragraph beginning at page 2, line 5 with the following:

US Patent Application Serial No. 10/033,027, entitled "PROGRAMMABLE MICROCONTROLLER ARCHITECTURE [PROGRAMMABLE SYSTEM ON A CHIP]," having docket number CYPR-CD00232, filed on 10/22/2001, and with inventor Warren Snyder is hereby incorporated by reference.

Please replace the paragraph beginning at page 12, line 17 with the following:

In one embodiment, the system 100 is configured to support microcontroller design. In another embodiment, the system 100 is configured to support programmable [system on a chip] microcontroller design. In yet another embodiment, the system 100 supports general chip design.

SUPPORT FOR AMENDMENTS

Support for the amendments herein can be found in the specification as originally filed (e.g., page 2, lines 5-8; page 7, lines 1-7) and in the related application Ser. No. 10/033,027. The present amendment intends to remove a reference to a trademark of Cypress Microsystems, Inc. (see, e.g., M.P.E.P. § 608.01(v)). No new matter is introduced.